

## **Lahiru Ariyananda**

Arizona Center for Integrative Modeling and Simulation  
1230 E. Speedway Blvd, Tucson, AZ 85721  
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### HIGHER EDUCATION:

#### **Candidate for Ph.D in Electrical and Computer Engineering**

University of Arizona, USA, August 2008 – to present

*GPA: 3.92/4.00*

#### **M.S in Electrical and Computer Engineering**

University of Arizona, USA, May 2007.

#### **B.S in Computer Engineering with Minor in Mathematics**

University of Arizona, USA, May 2001.

*Honors and Magna Cum Laude*

Western Michigan University , Kalamazoo, Michigan, USA.

August 1997-December 1998.

#### **Diploma in Computer Science**

Institute of Technological Studies, Sri Lanka /

University of Houston, Clear Lake, USA.(*affiliate*)

### WORK EXPERIENCE :

10/02 to date

#### **Lecturer**

University of Peradeniya, Dept of Computer Engineering, Sri Lanka.

- Duties involve Dissemination of Knowledge, Dept Administration, Coordination of Courses, Liaison between Industry, Coordination/Supervision of Academic/Intramural activities
- Coordinator /Taught Courses in
  - Computer Networking ( Final Year, 30-50 students)
  - Introduction to Computers ( 1<sup>st</sup> Year, 320 students)
  - Computer Programming ( Java, 2<sup>nd</sup> Year, 90-160 students)
  - Digital Art ( 3<sup>rd</sup> Year, 40-150 students)

02/07 to 06/07

05/09 to 08/09

#### **Research Engineer**

Arizona Center for Integrated Modeling and Simulation  
University of Arizona, US

08/05 to Present

#### **Graduate Teaching Assistant**

University of Arizona, Dept of Electrical & Computer Engineering ,USA

- Have won several competitive awards for teaching
- Team Lead for group of 3-5 personnel, Sp 07,Fa08 – Fa10
  - Computer Programming for Engineering Application
  - Class sizes upto 180 first year students

06/06 to 12/06  
06/10 to 08/10

### **Research Engineer**

RTsync Corporation, Phoenix, USA.

- Worked as a team member of the *GenetScope* project
- Research and teaching FDDEVS

08/01-03/02

### **Visiting lecturer**

American College of Higher Education ( Kandy, Sri Lanka)

- Has conducted preparatory courses for A+ ( certification for PC technicians) MCP (Microsoft Certified Professional )

02/00- 12/00

### **Undergraduate Research Assistant**

Radiology Research Laboratory, University of Arizona.

- Wrote PCI target applications for an FPGA based data acquisition system for a Gamma Ray Imager
- Programming done in C++, VHDL, and Lab View
- Simulation and Synthesis tools used :Work View Office Suite, FPGA Express, Leonardo Spectrum. Lucent ORCA Foundry.

SELECETD  
COURSEWORK :

Software Engineering , Artificial Intelligence, Distributed Multi-Paradigm Simulation Systems , Software Tools for Engineering Applications, Engineering for Computer Based Systems, Distributed Computing Systems, Probability & Random Processes, Micro Electronic Packaging Materials, Discrete Event Simulation, Computer Networks and Protocols, Circuit Theory, Circuit Analysis, Computer Aided Logic Design.

MASTERS THESIS :

Graphical User Interface Representation and Generation Using System Entity Structures, Summer 07. Adviser : Prof B.P Zeigler

SELECTED PROJECTS :

#### **Simulation of a Flexible Manufacturing System**

- Leader of a 3 Member team
- Coded in ODEVS ( C++)

#### **Online Multimedia Database**

- Team Leader of a 3 member Graduate Student group
- Written in Java, JSP, Html, Sql

#### **GenetScope**

- Large Professional team project
- Developed for Defense Information Systems Agency, Joint Interoperability Test Command , Fort Huachuca, Arizona,USA.
- Written in Java using DEVS .

**Design and Simulation of a Dynamic Traffic Controller**

- Written in Java

**Designing an Electronic subsystem for a SPECT imager**

- Mainly software based using VHDL & LabView

**Designing a package for a next generation Microprocessor.**

## PROGRAMMING

## LANGUAGES :

C/C++, Java, Visual Basic, Html with CGI, PostScript, Shell Scripting, Perl, VHDL, LabView, Pascal

## SPECIALIZED

COMPUTER :  
APPLICATIONS

DEVJAVA , FDDEVS, Arena, AnyLogic, Workview Office Suite, Leonardo Spectrum, PSpice, AutoCad, Adobe Photoshop, Flash , Rational Rose/Visual Paradigm, MPI

## SELECTED PAPERS

## /CONFERENCE :

U. Bulumulla, R. Navarathna and G. Munasinghe and L. Ariyananda, "*Determining Capital Market Efficiency in the Colombo Stock Exchange (CSE) using Technical Analysis and the Artificial Neural Networks*", Second International Symposium at the University of Sabaragamuwa, July 2008, Sri Lanka.  
Supervised by myself. ( **won the gold medal for the best project in Student Forum in Engineering**)

## PROFFESIONAL

## QUALIFICATIONS :

Followed a certificate course in Teaching Methodology  
University of Peradeniya.

## AWARDS/HONORS/ MEMBERSHIP :

**Outstanding Graduate Teaching Assistant**

College of Engineering, University of Arizona, Sp 09

- Very Competitive. Overall winner amongst 13 Departments with over 3000 students

**Outstanding Graduate Teaching Assistant**

Dept of Electrical and Computer Eng, University of Arizona, Sp 09

- Competitive. Department student population over 700

**Award for Excellence at the Student Interface**

University of Arizona. Sp 10.

- Elected through student participation and voting

**Graduate College Fellowship**

University of Arizona, 2005-2006

**Tuition & Registration Scholarship ( full)**

University of Arizona, 2005-2007

**Recipient of Arizona Board of Regents Scholarship**

1999 Jan –2001 May

- Very Competitive award amongst 2300 International students

**Deans Honorable Mention List**

Spring 99, Fall 99, Spring 2000, Fall 2000, **University of Arizona**

**Awardee of Scholarship** of the College of Engineering & Applied Sciences

Western Michigan University, 1998-99 school year

Member :

Tau Beta Pi – Engineering Honor society  
 Honors College, University of Arizona  
 National Society of Professional Engineers  
 Golden Key National Honor Society

**EXTRA-CURRICULAR ACTIVITIES  
& COMMUNITY SERVICE :**

- Mentor , Arizona Assurance Scholars program, 2010-2011
- Member of the Working committee of Industry day, 2004  
Faculty of Eng, University of Peradeniya, Sri Lanka
- Host/Master of Ceremonies  
Peradeniya University Research Sessions, 2007
- Coordinator of the Engineering Faculty E-Souvenir CD for Industry Day 2004
- Actively involved in Social Welfare societies ( Sri Lanka)
- Hosted/Coordinated/Participated in events extending multi cultural diversity, in Tucson Arizona.

REFERENCES :

Available upon request